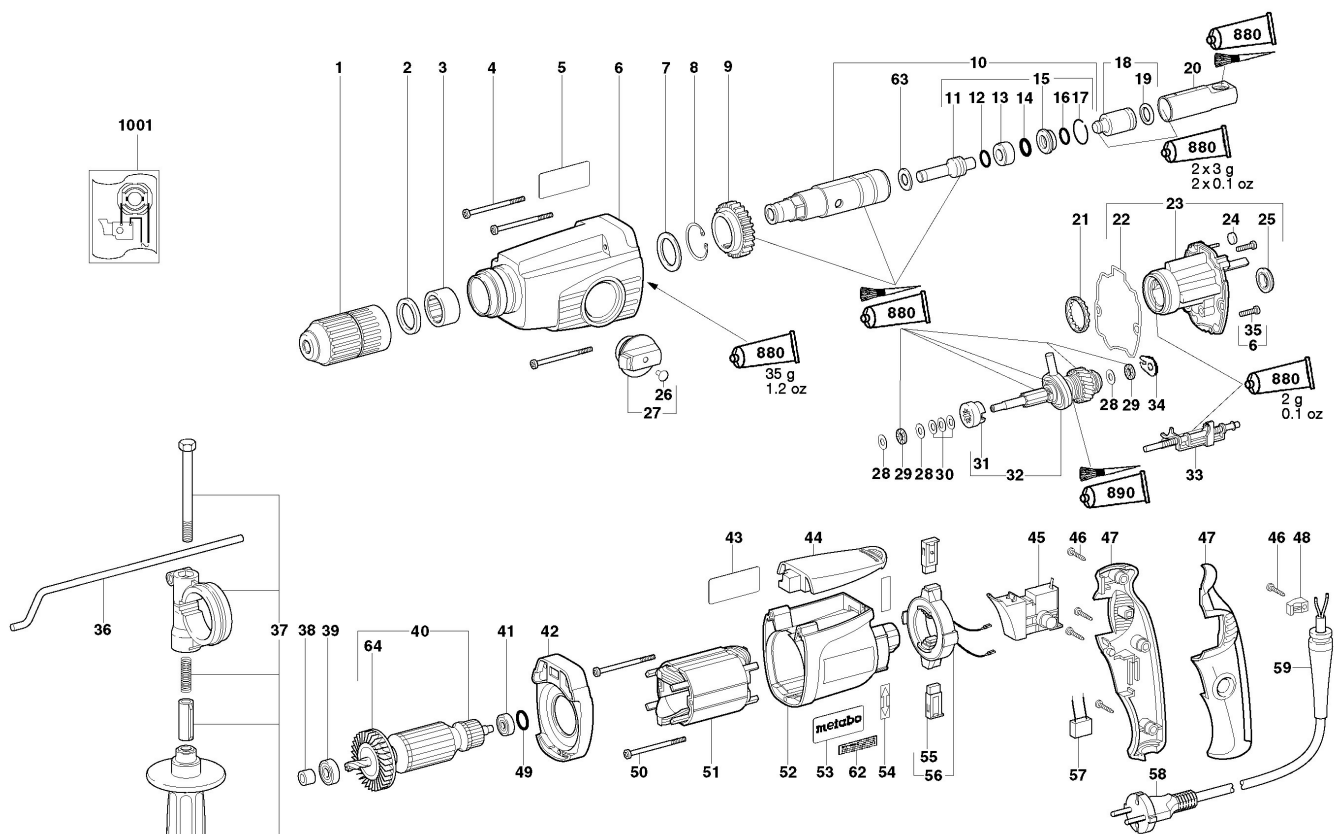
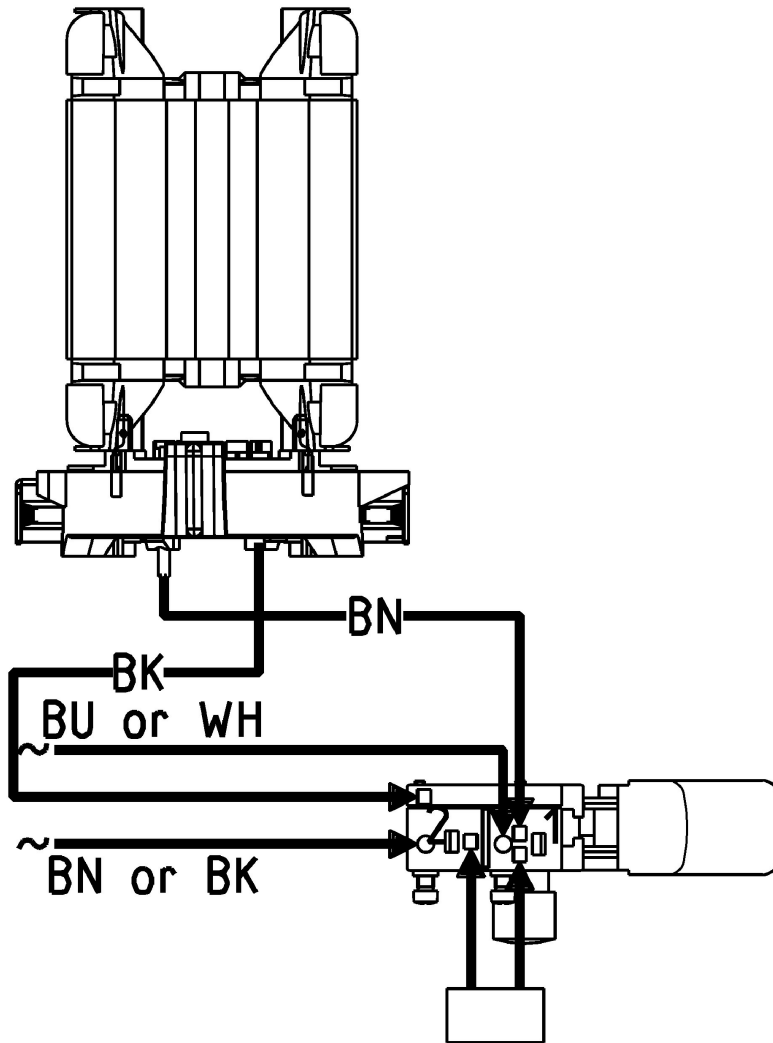




SET 	11	14	17	20	24	880
	12	15	18	21	26	
	13	16	19	22	55	

61



Circuit Block Diagram

