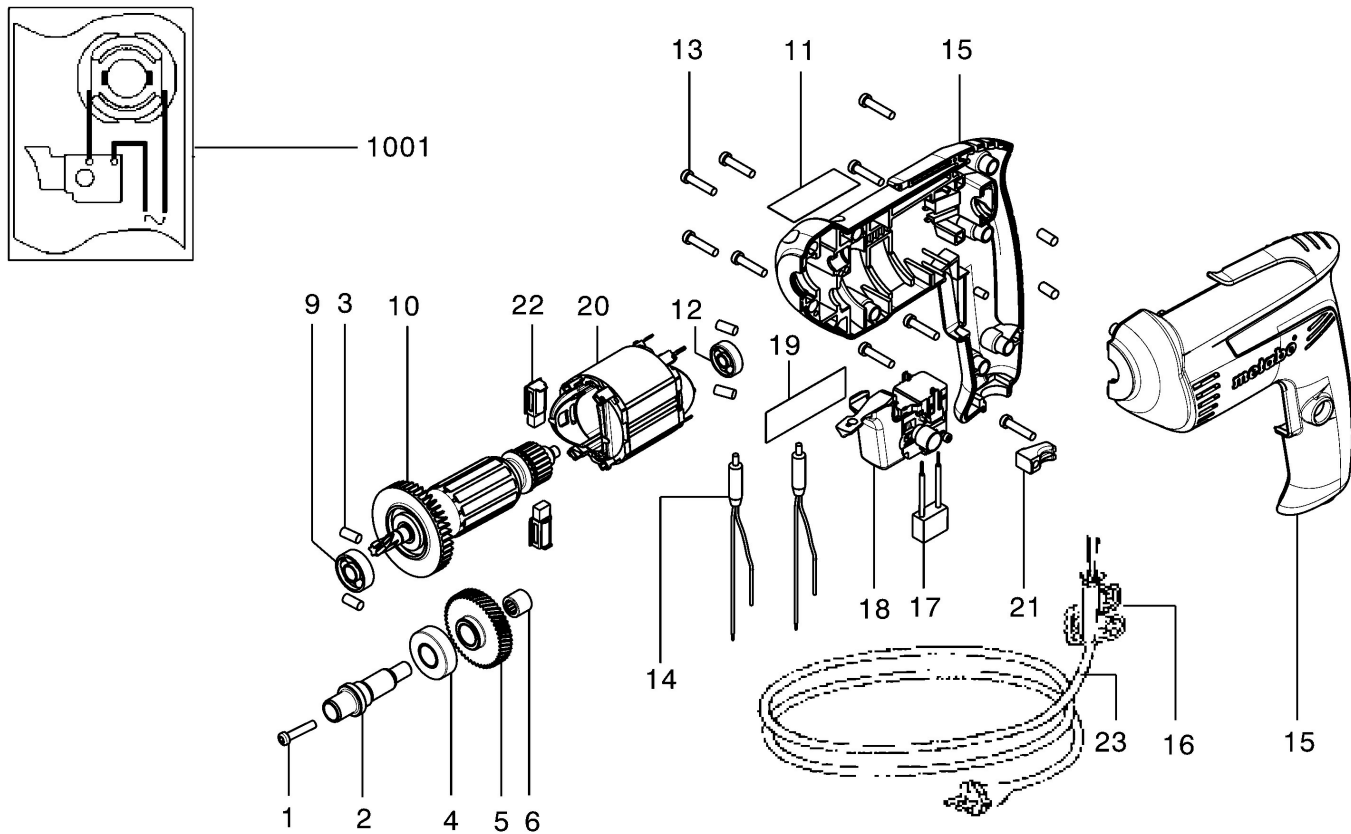
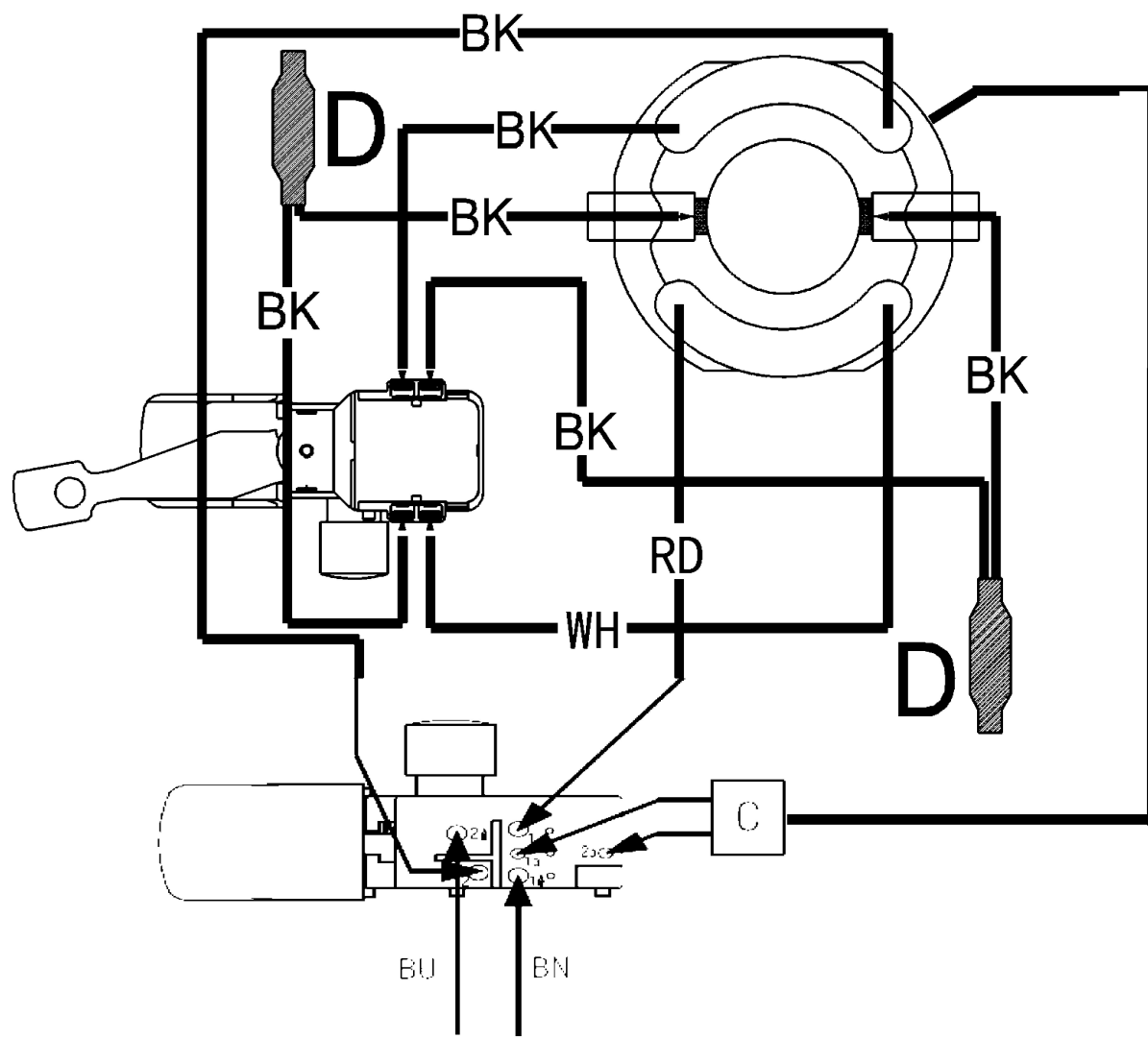
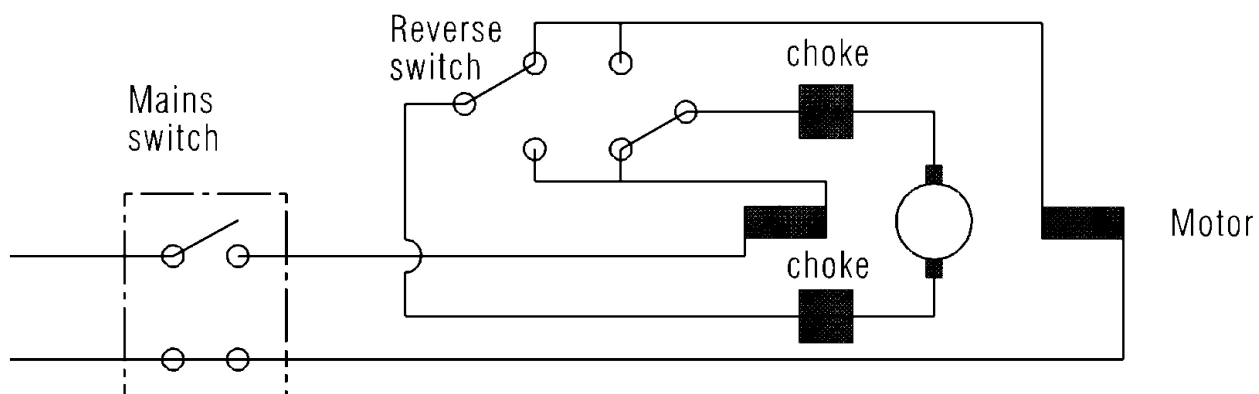
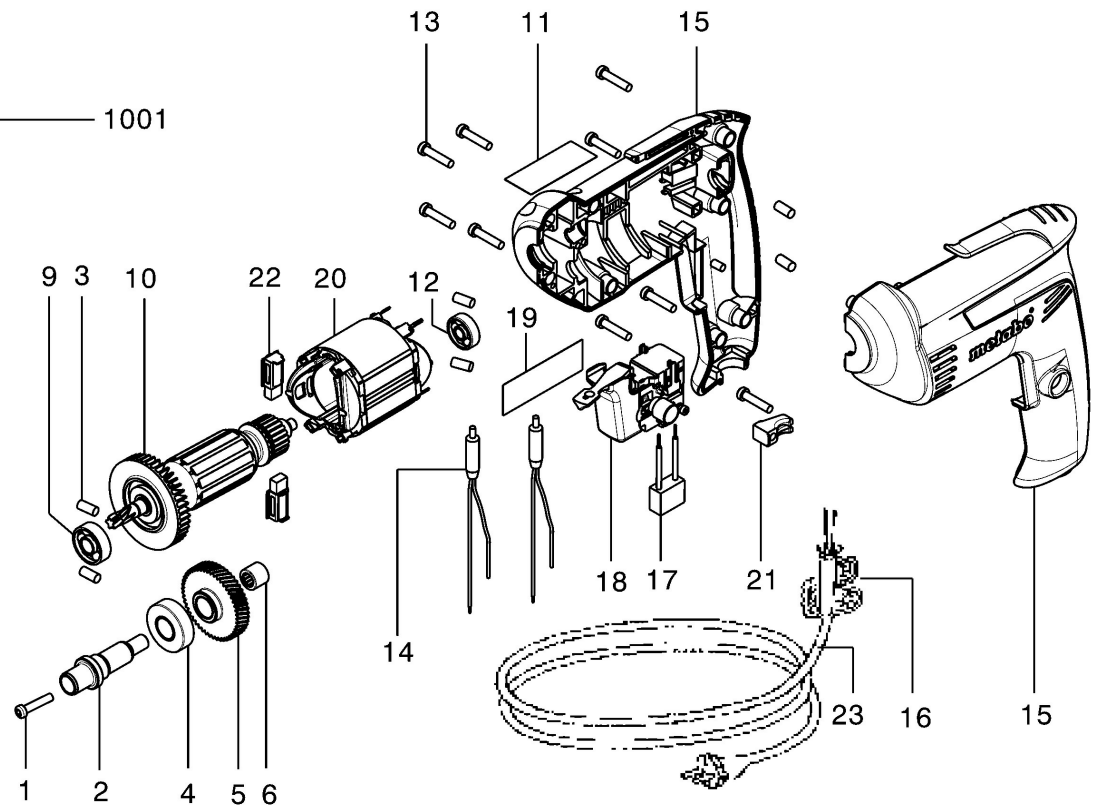
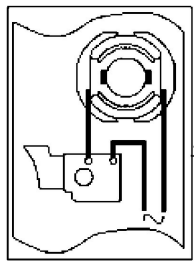


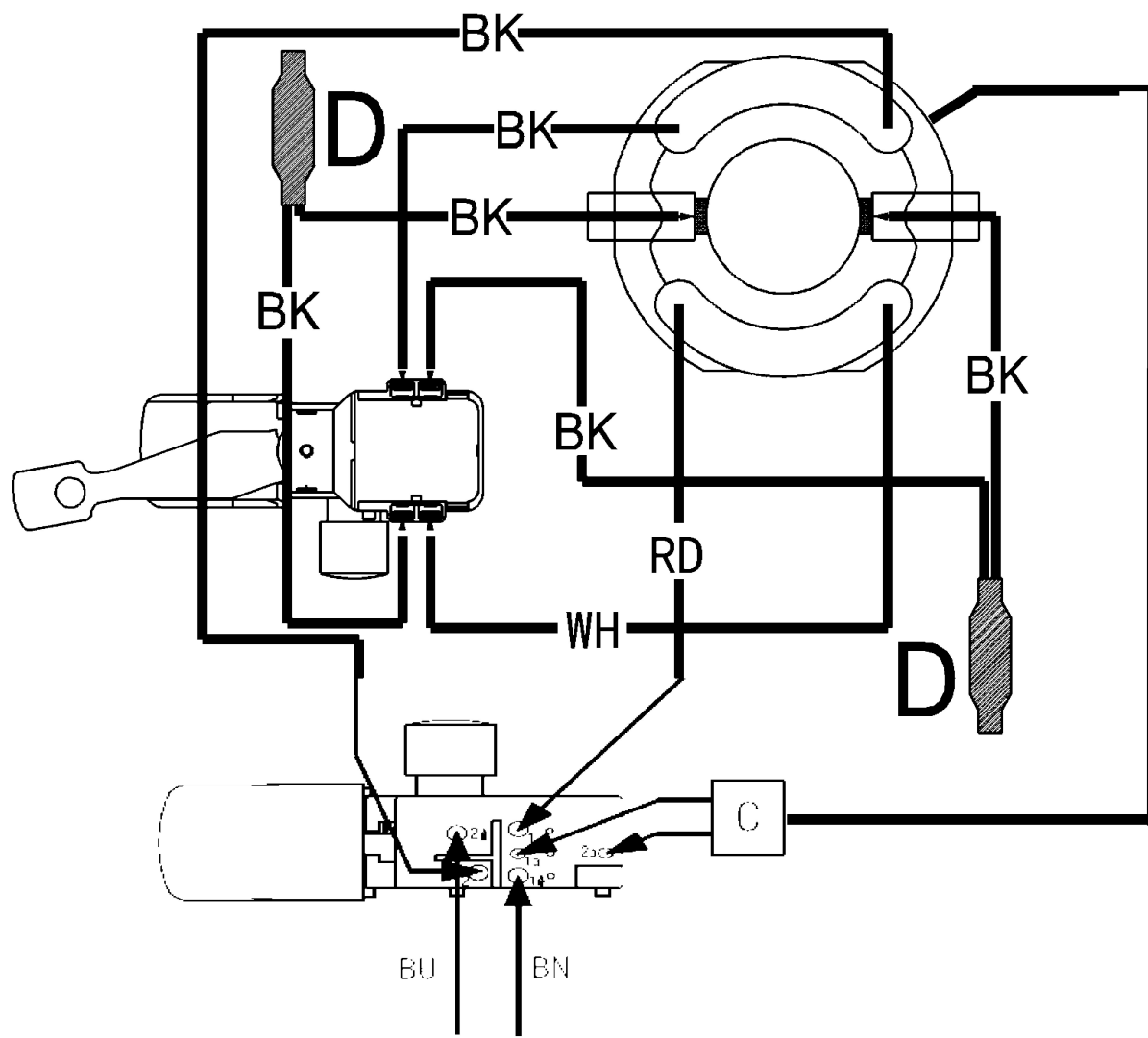




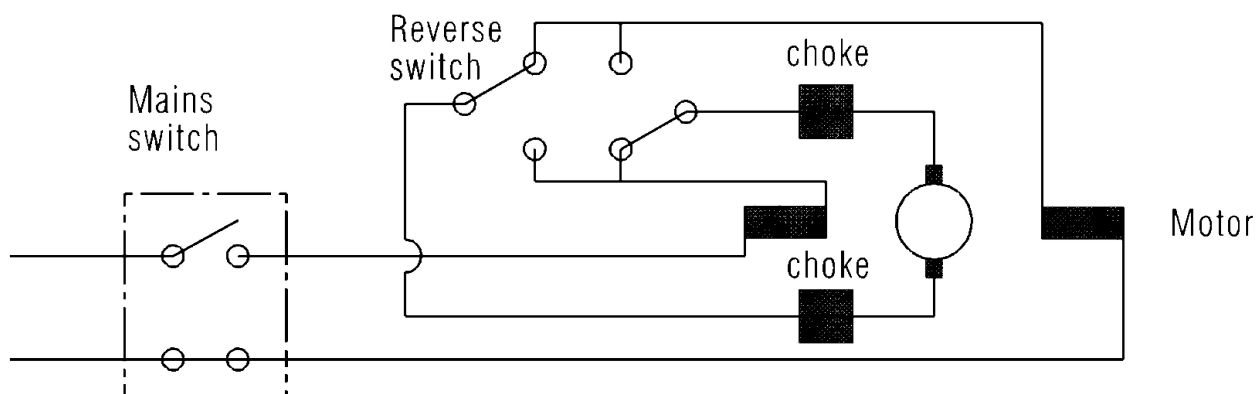
a# Circuit Block Diagram



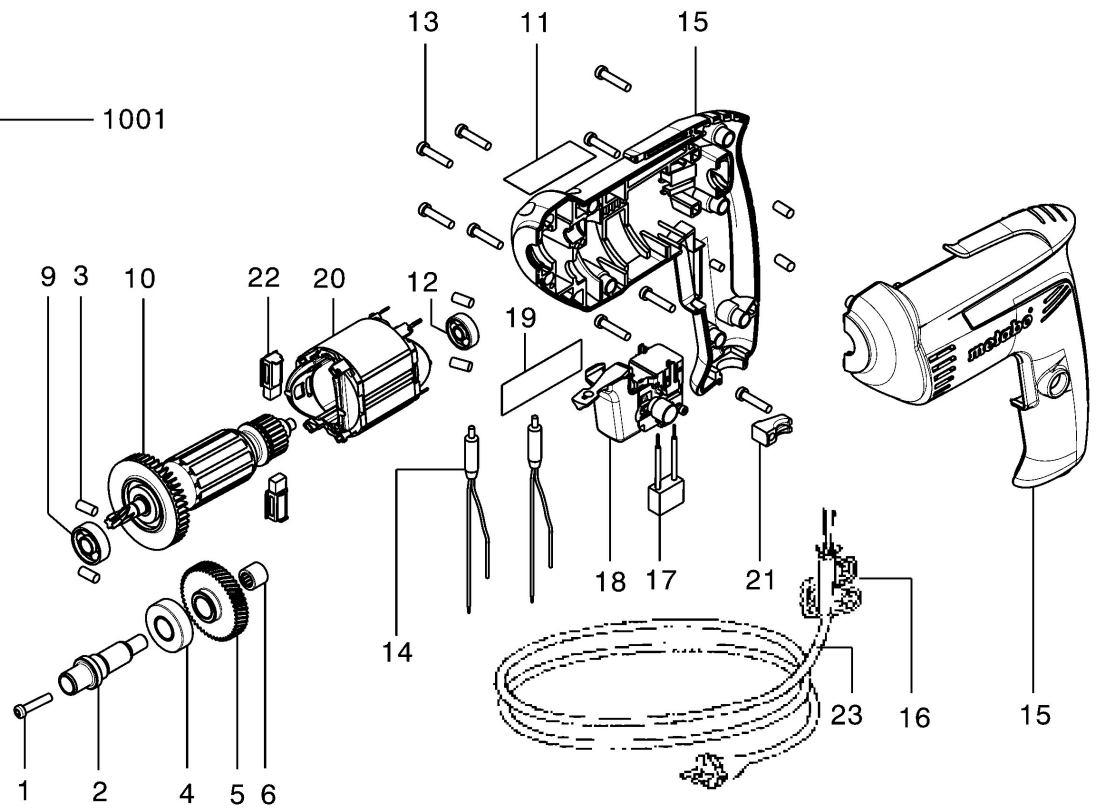
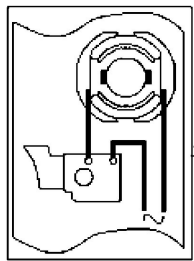


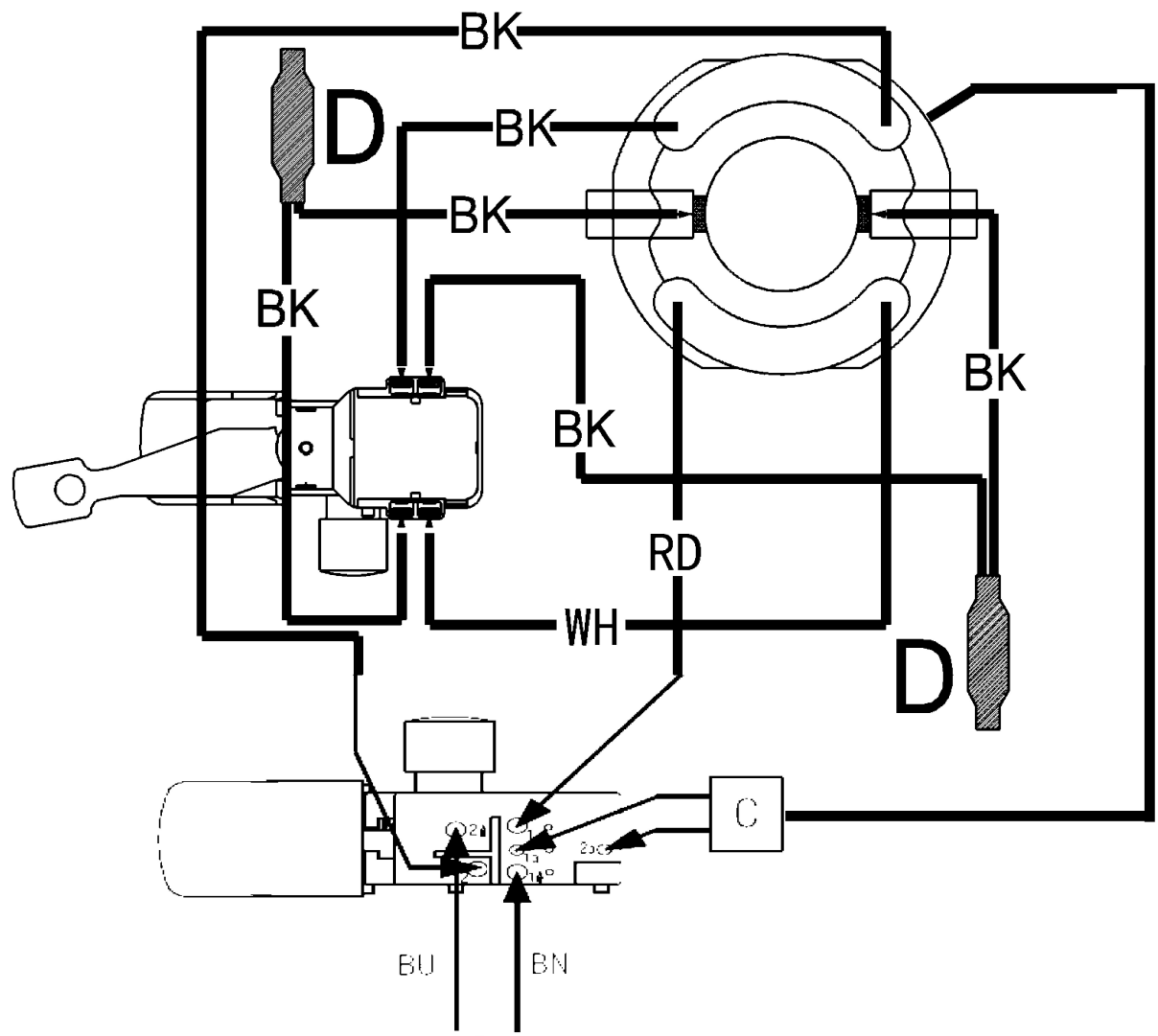


a# Circuit Block Diagram

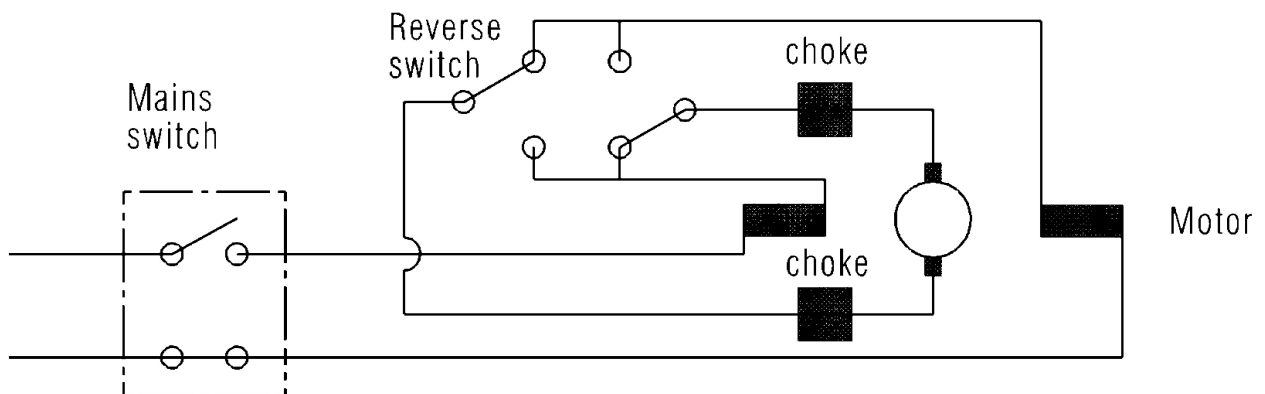


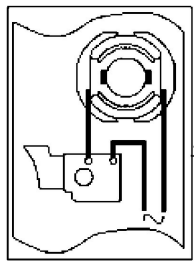




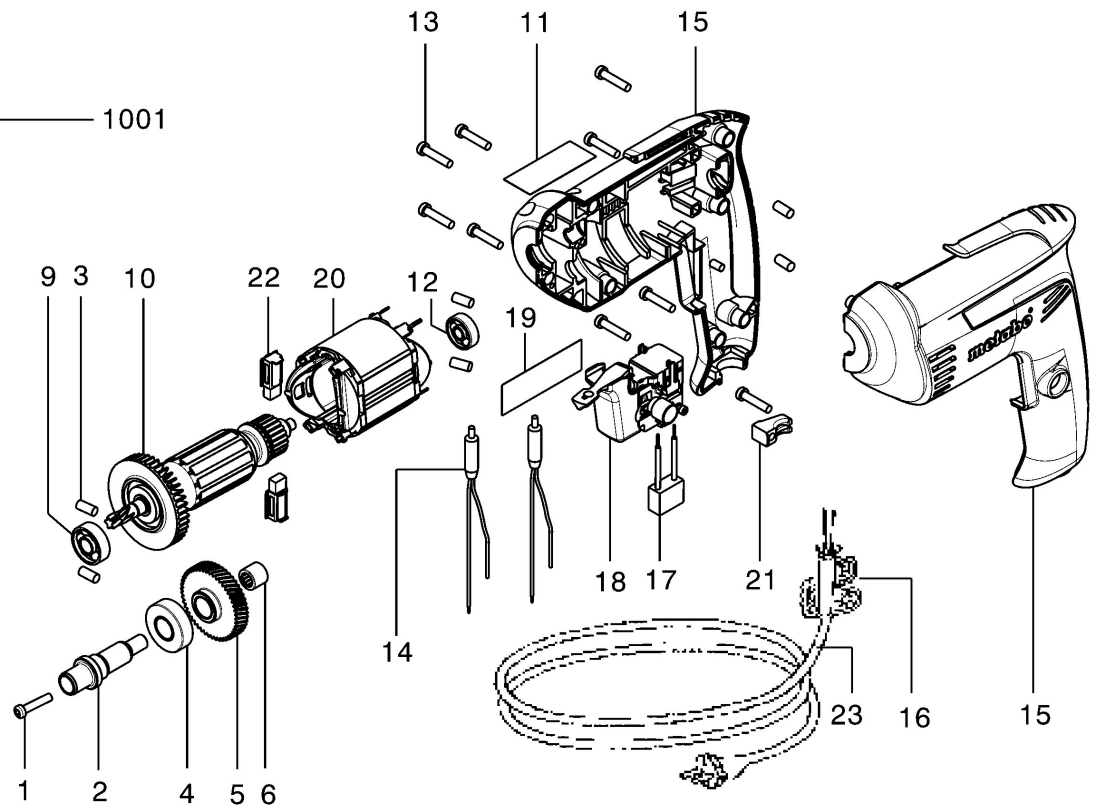


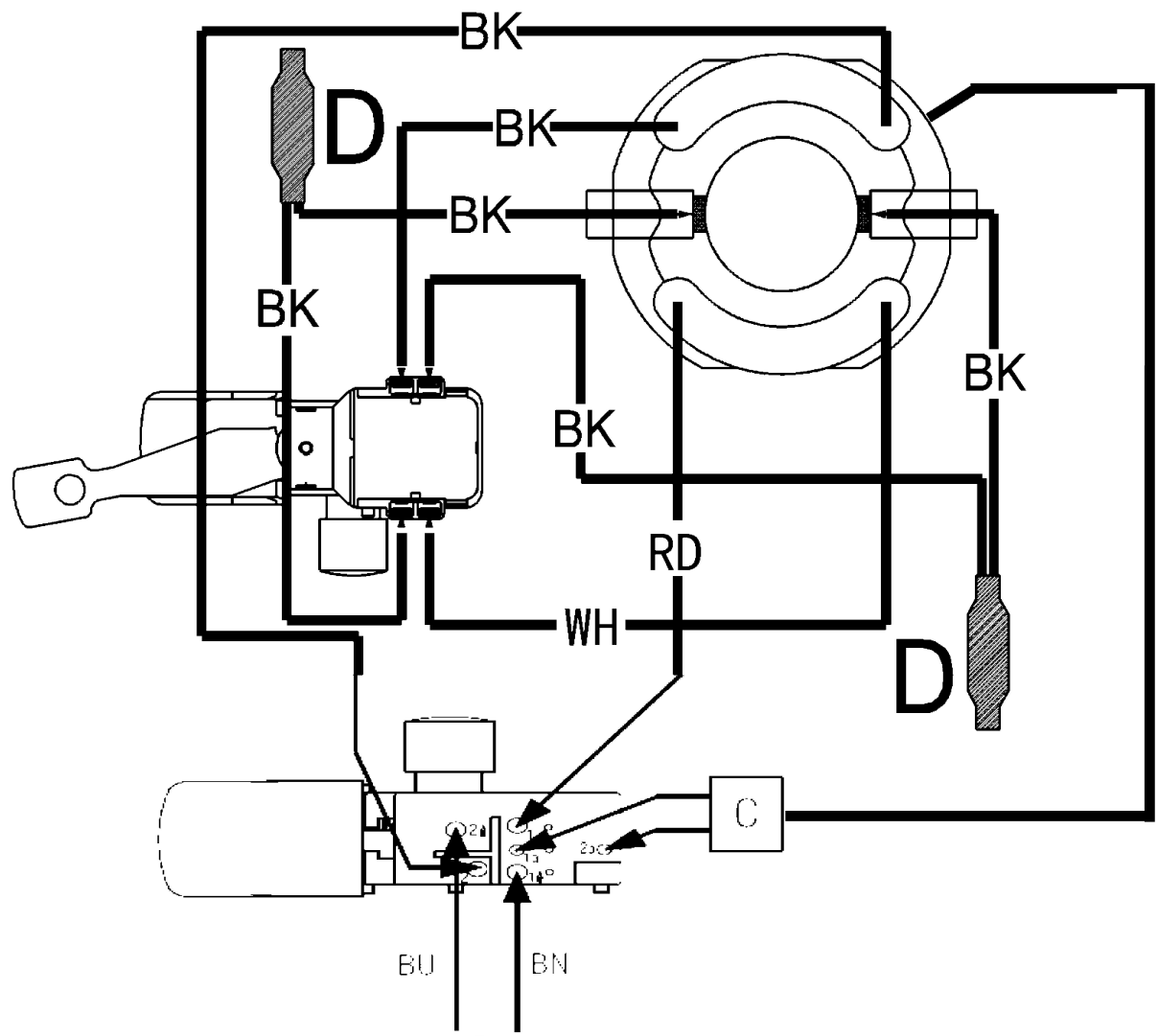
a# Circuit Block Diagram



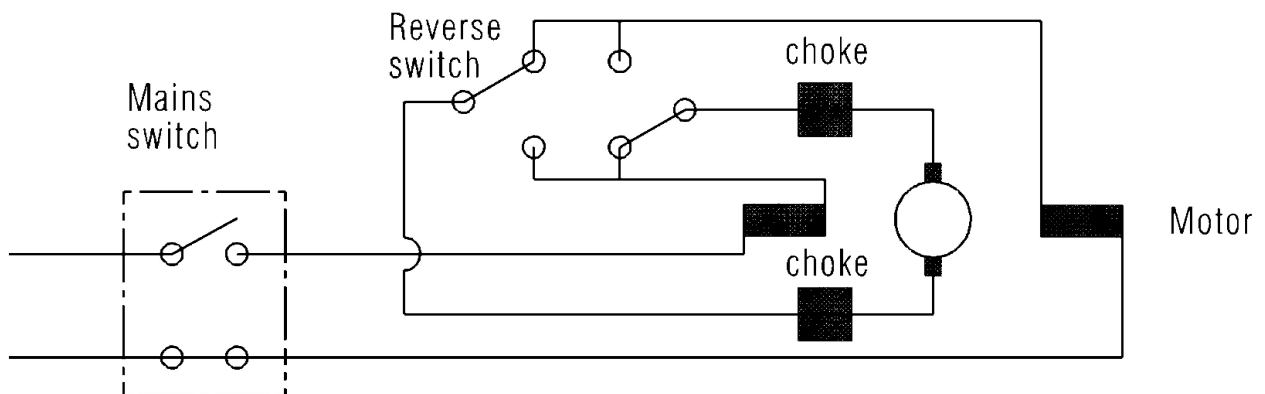


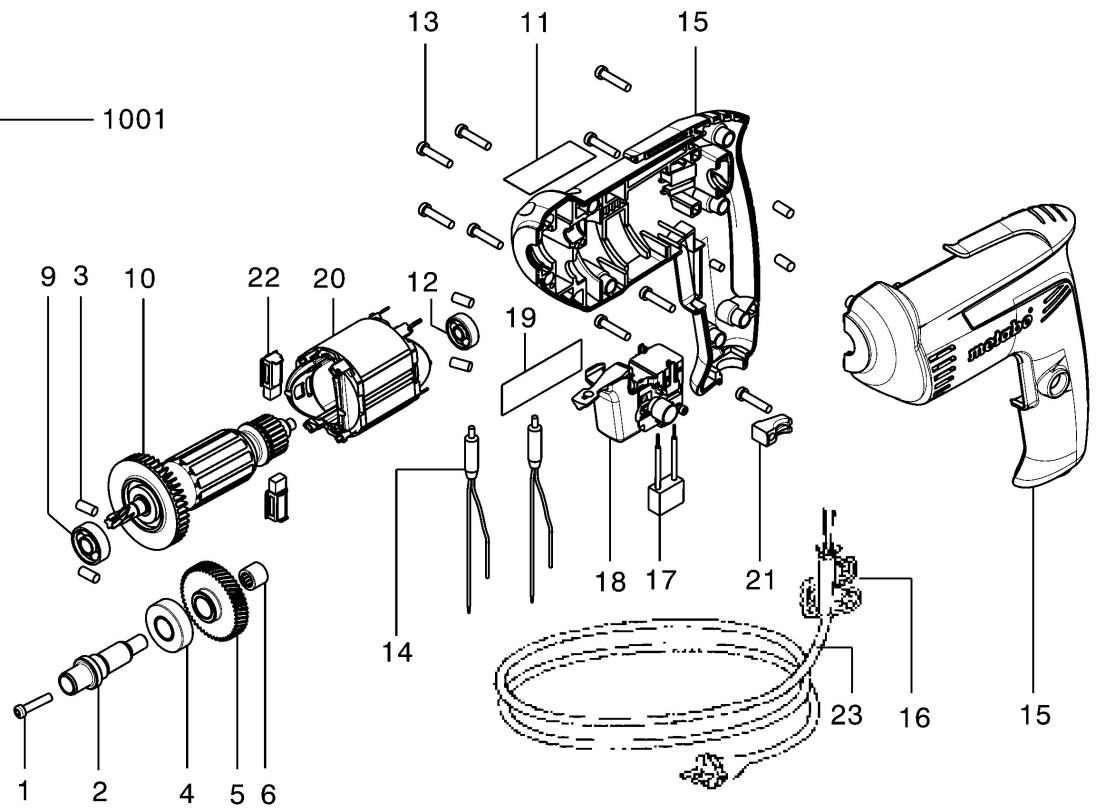
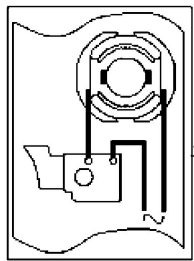
1001

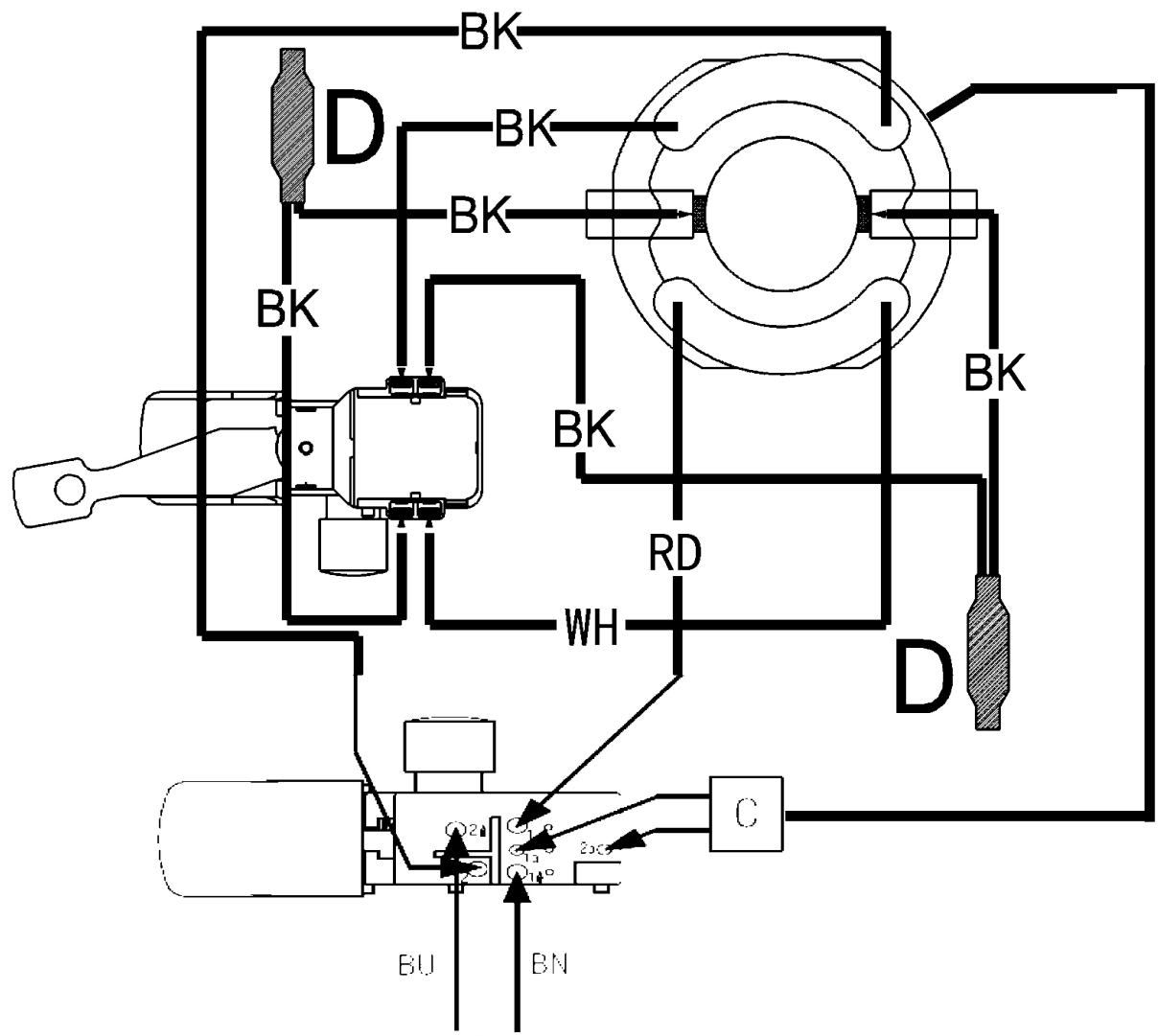




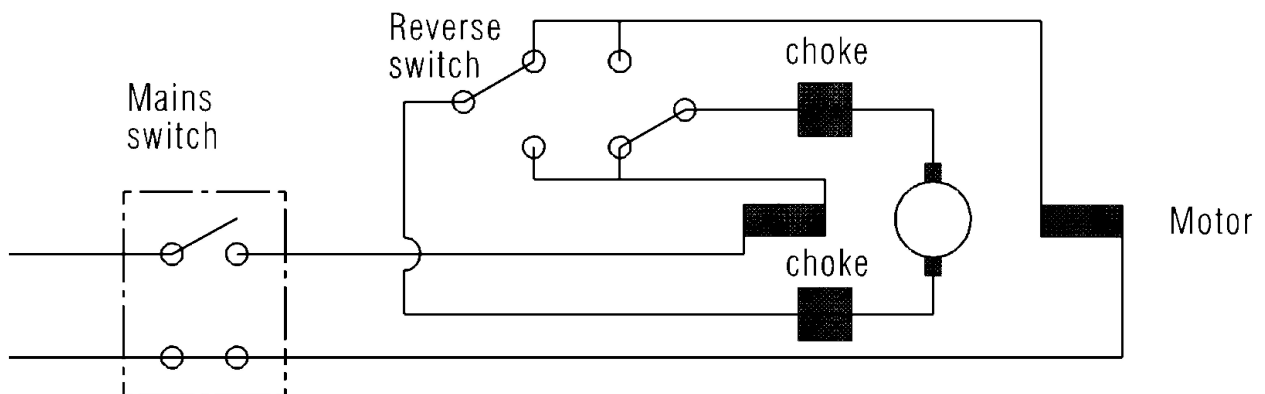
a# Circuit Block Diagram

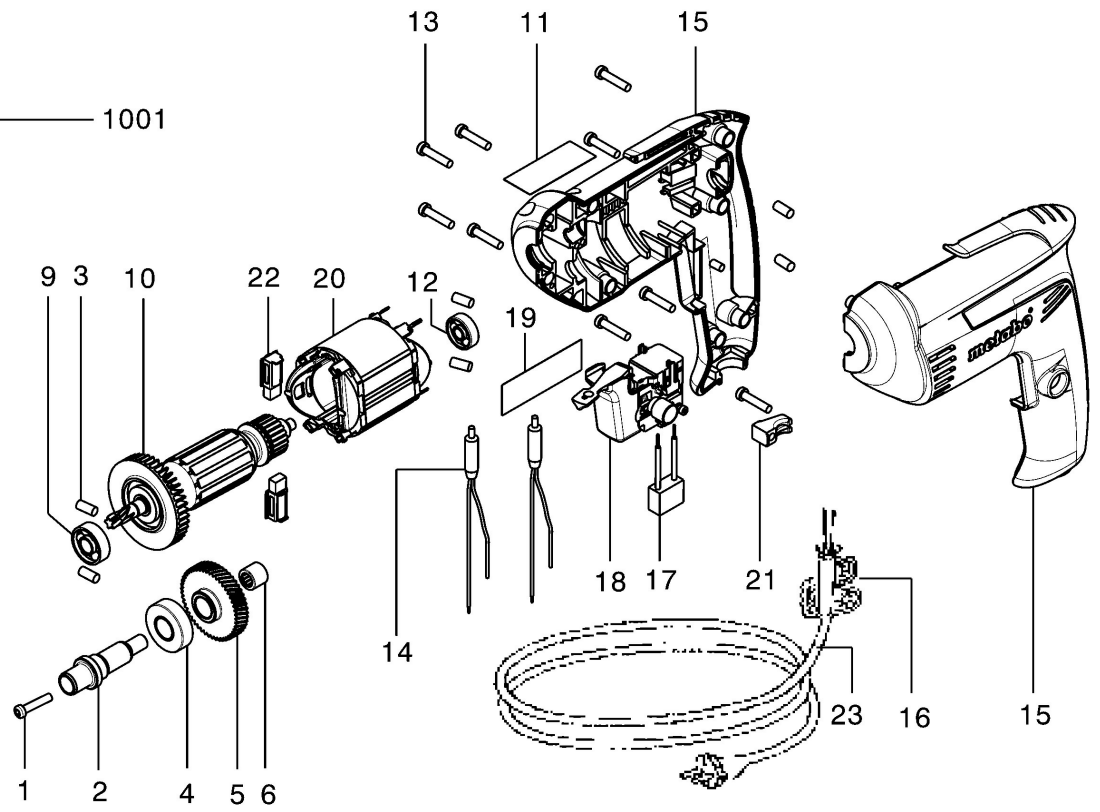
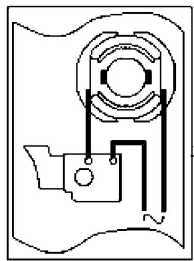


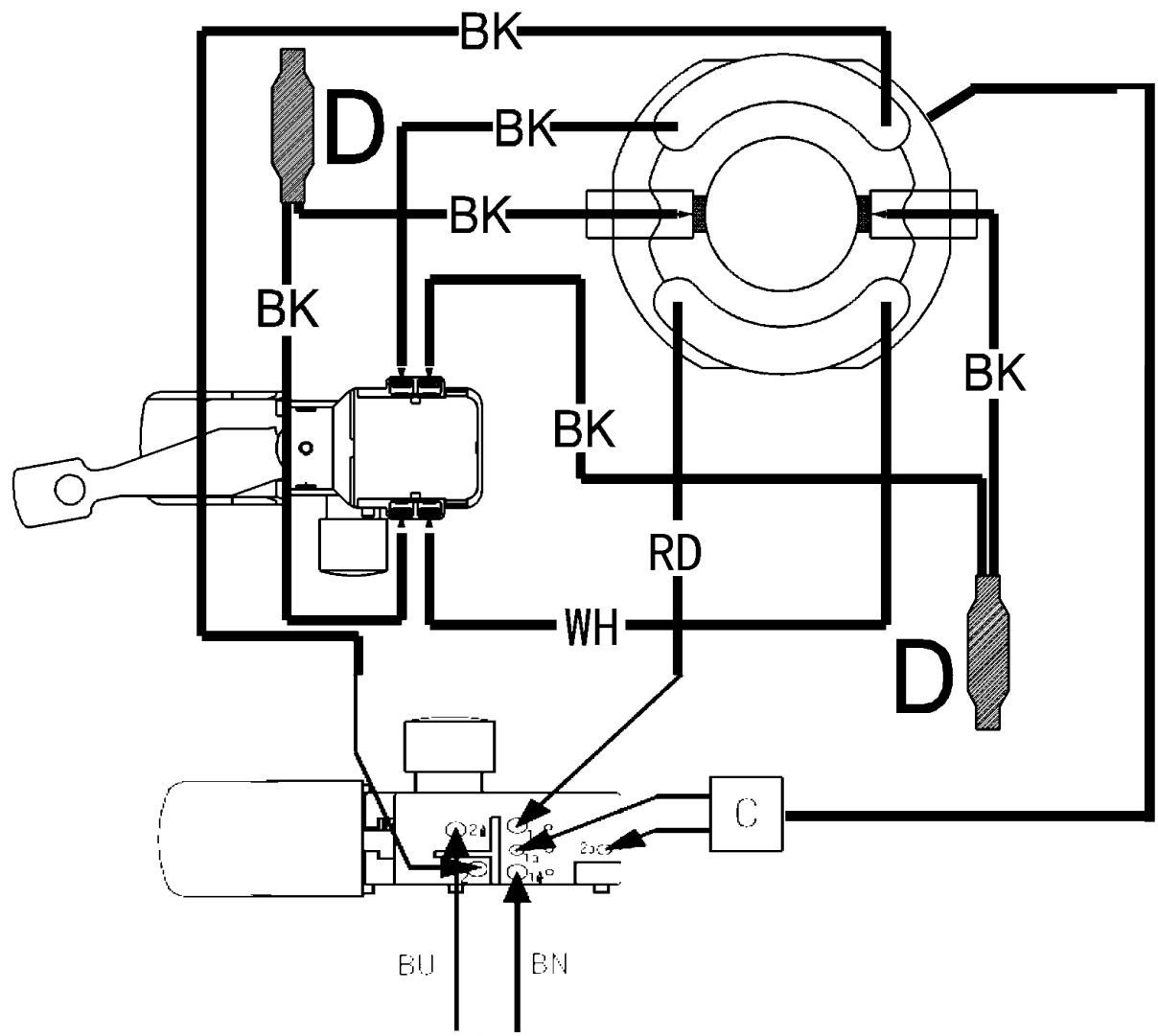




a# Circuit Block Diagram







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