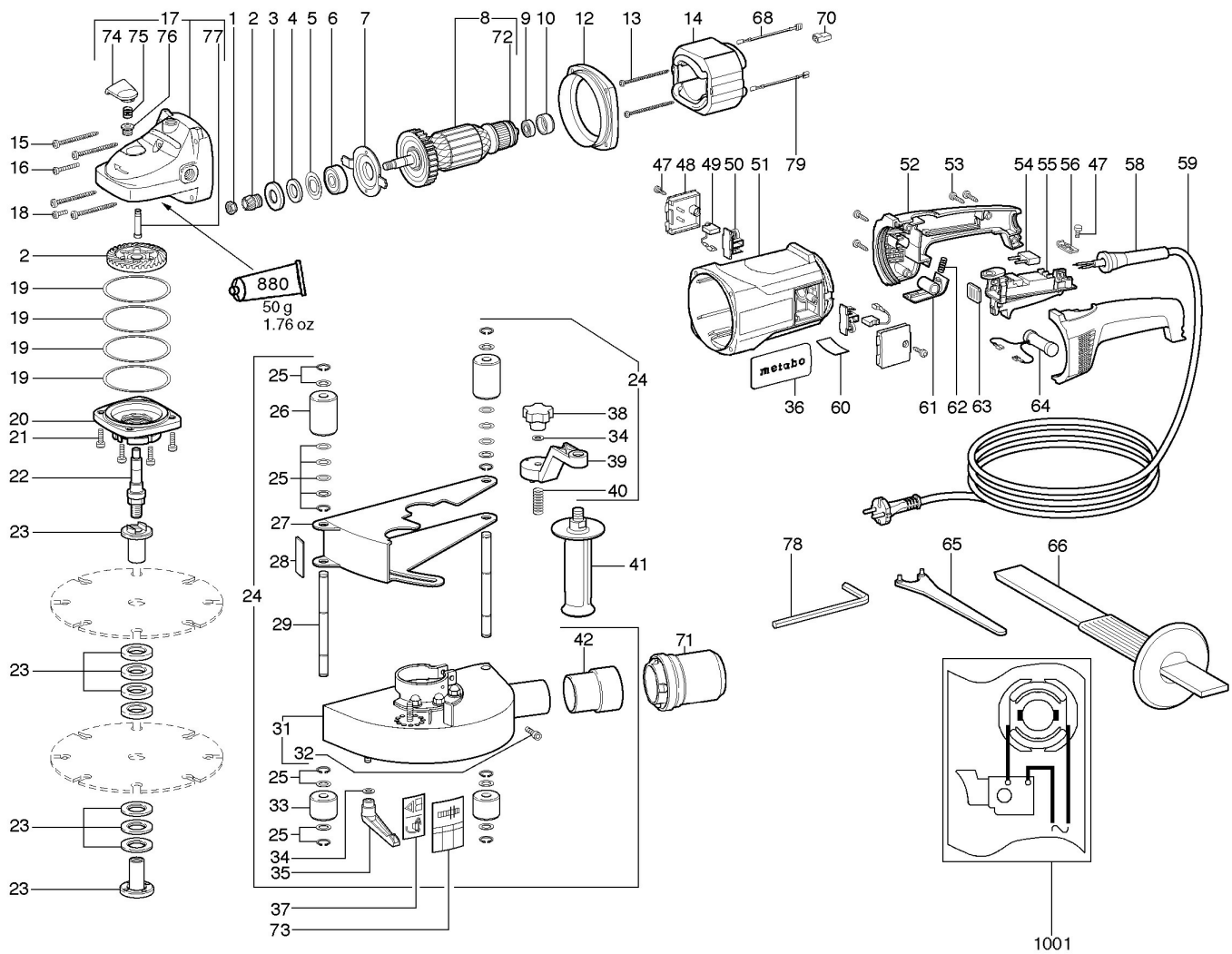
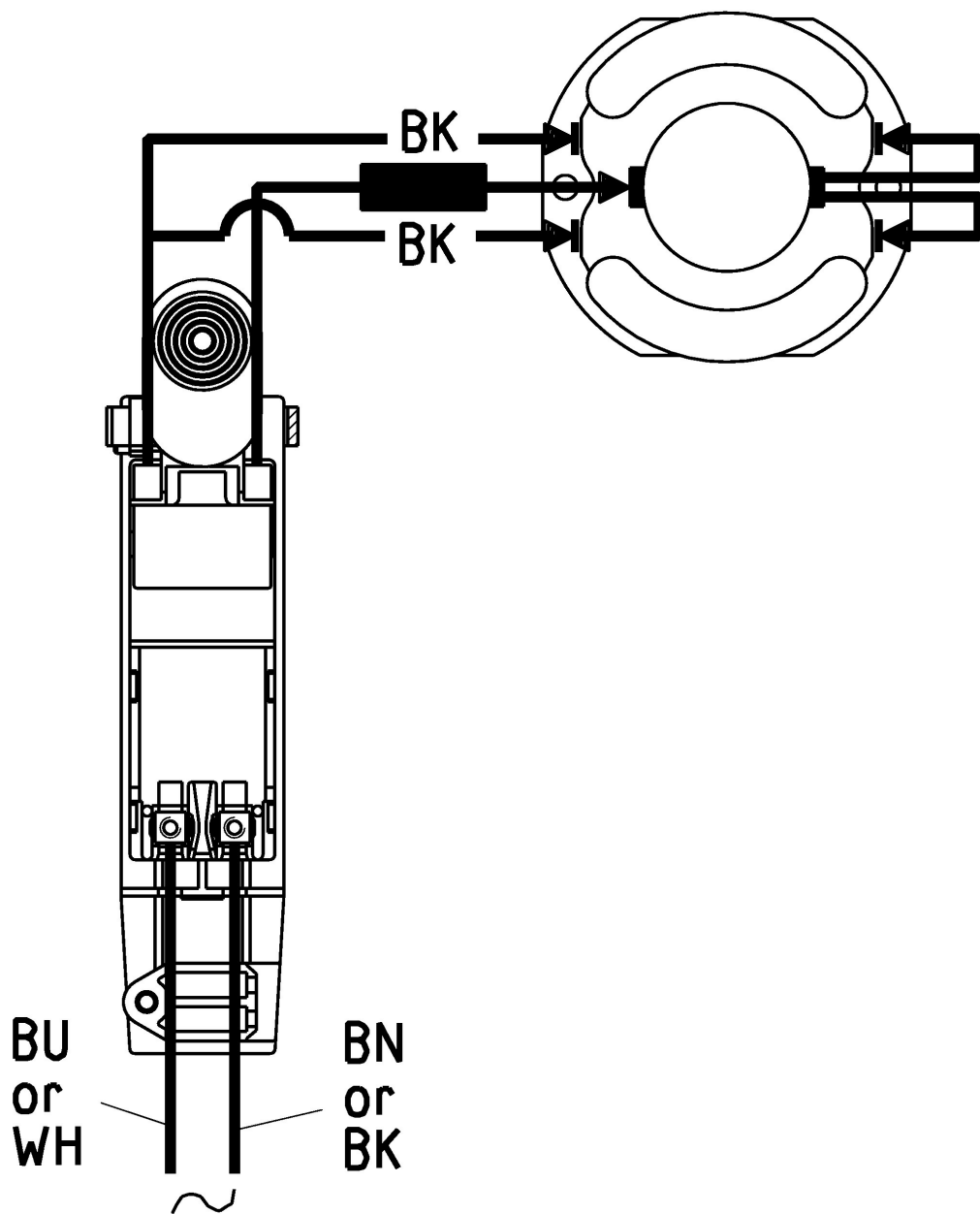






Circuit Block Diagram

